

EXAMINER'S OFFICE LETTER

FEB 22 2007

(Mailing Date: FEB 28 2007)

To: Applicant (Agere Systems Guardian Corp.)

Examiner : H. ABE

"GATED CLOCK RECOVERY CIRCUIT"

Patent Application No. 2001-353920

The above-identified application is to be refused for the reason as put down. A reply to the present office action must be filed before MAY 31, 2007 (extensible up to three months (maximum)).

= NOTE =

Reason

The inventions recited in Claims 1-13 are unpatentable under Article 29, Paragraph 2 of the Japanese Patent Law because of obviousness from the listed prior art Reference 1.

Remarks:

Particularly, Fig. 1 and the descriptions with reference to this drawing in the specification in Reference 1.

If any reason for refusal is newly found, it will be notified.

= List of Prior Art Reference =

Reference 1 : European Patent Application Publication No. 654907

<Record of Searched Results for Prior Art References.>

· Searched Field: IPC H04L 7/033

· Related Prior Art References:

JP Laid-Open Gazette No. 02-076418

JP Laid-Open Gazette No. 08-186490

JP Laid-Open Gazette No. 07-202873

JP Laid-Open Gazette No. 08-237231

The above related prior art references are not considered to deny the patentability of the present invention.